

output of said digital filter, said controlled reference current value being the reference current value of said D/A conversion circuit.

5. (Amended) A modulation semiconductor integrated circuit device according to claim 1, wherein said phase-locked loop includes a variable counter circuit which counts the oscillation output of said oscillation circuit, and a register which sets a value to be counted by said variable counter circuit, the base frequency being changed in response to the alteration of the value set in said register, the reference current value being controlled in accordance with the value set in said register.

6. (Amended) A modulation semiconductor integrated circuit device according to claim 1 including a trimming circuit which adjusts the reference current value.

7. (Amended) A modulation semiconductor integrated circuit device according to claim 1, wherein said voltage-controlled oscillation circuit includes a first variable capacitance means and a second variable capacitance means, and has its oscillation frequency varied in response to the